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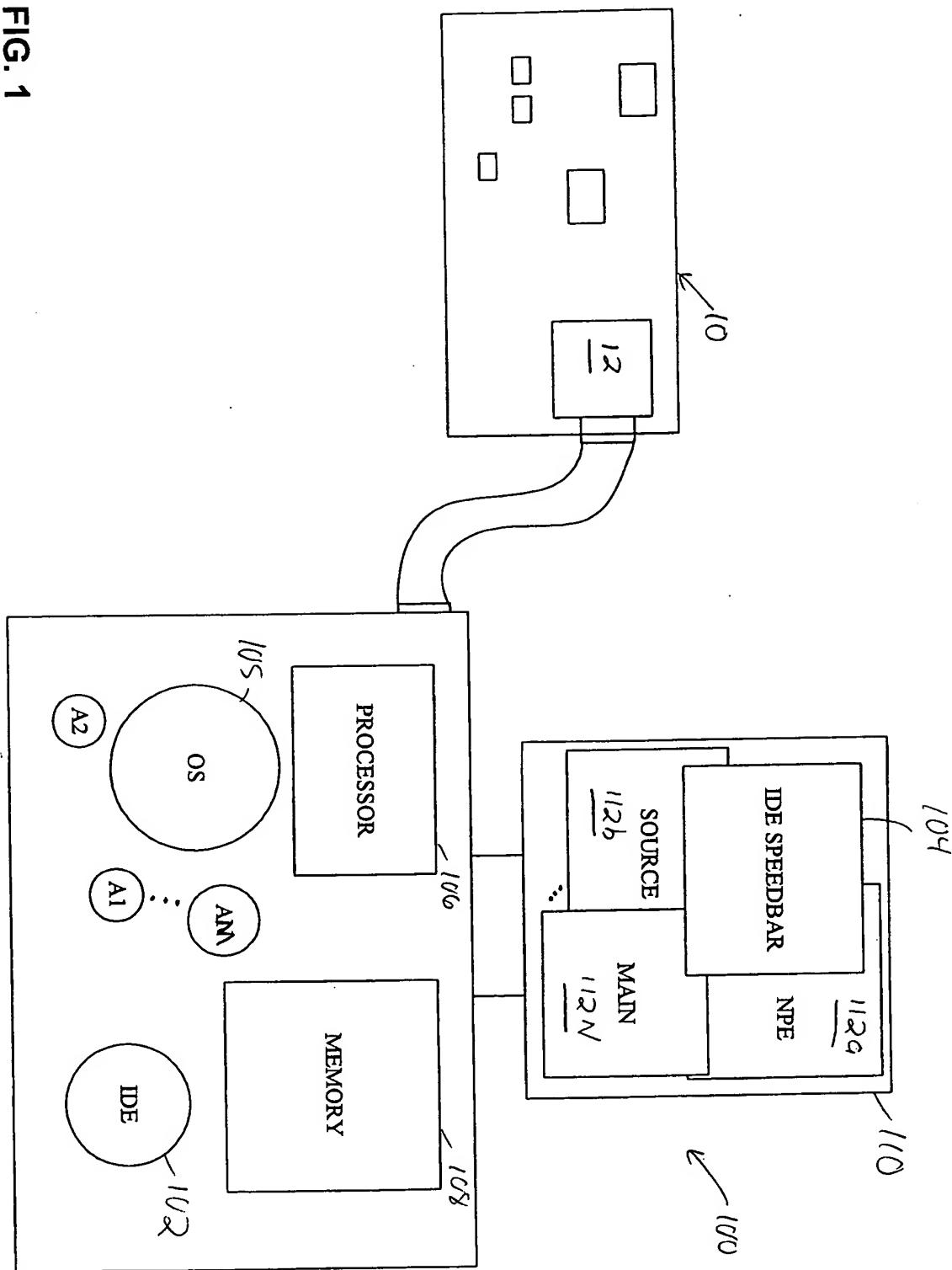
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FIG. 1



Title: SYSTEM AND METHOD FOR EMBEDDED PROCESSOR

FIRMWARE DEVELOPMENT

In re Patent Application of: Dan M. White

Docket No.: Intel-017PUS

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200

westport_npe0_sw_tb_vmod.syst...

File Project History Units View Help

code segment "run", length = 54
404 SYMBOLS, 0 UNRESOLVED.

C:/npe_tools/tools/cmodels/npe0_sw_tb/win32/ttxpcr.s:143: WARNING code segment does not end
in an unconditional branch:
nop

54 INSTRUCTIONS, 3 BASIC BLOCKS, 18.000 INSTRUCTIONS PER BASIC BLOCK.

Loading segment "run" at 0x0000 - 0x0035
Loading data segment "data" at 0x0000

0 PSM2 ASSEMBLER ERRORS
8 PSM2 ASSEMBLER WARNINGS

ExecShowStderr c:/npe_tools/tools/bin/windows/psm2a -l c:/npe_tools/tools/include/psm2 -G -2.1
-DNPE0_BUILD_0_1 C:/npe_tools/tools/cmodels/npe0_sw_tb/win32/ttxpcr.s -o txpcr
npe0 init_instruction_memory 4096 txpcr.o
npe0 init_data_memory 8192 txpcr.data

FIG. 3

202



expers

File: c:\models\neclzw.lif\m32\exp

Step 1: C7 | -1(FS) | -1 | -10 | <100 | 1000 | 1000 | Step 1: 2 | 0000

```
.dreg32 xxx = d0
    mov32 xxx, #4
#include "...\\..\\..\\include\\psm2\\cudjoe_A_psm.h"
.begin
    ireg myireg=i2 - 406 418
    pointer myireg @ mypacket.pkthdr2
.dreg32 xxx = d4
    mov32 myireg, #4 - 410
    mov32 xxx, #8
.begin
    CodeLabel3 - 414
    ireg myireg=i4 - 408
    nop
    nop && LDUR=1
    mov32 d0, ##CodeLabel3 - 404
    mov32 myireg, #8 - 412
    mov32 d0, #4
.end
.end
    nop && LDUR=1
    mov32 d0, ##ConfigTable2 - 416
    xfrmrc [i0, #0] && HSS_WrCond ; Se
    xfrmrc [i0, #0]
    nop
    nop && LDUR=1
003: nop
004: nop
005: mov32 d0, ##0x3
006: mov32 d0, #0x8
007: mov32 d0, #0x4
008: nop
009: mov32 d0, ##0x66
00A: xfrmrc [i0, #0]
00B: nop
00C: nop
```

400

402

404

412

416

FIG. 4

204

PSM data for psma									
File Edit Mem Dump Detach View Help Status									
0x000d 0x1000000b									
0123456789ABCDEF0123456789ABCDEF									
XXXXXXXXXXXXXX									
0123456789ABCDEF0123456789ABCDEF0123456789ABCDEF									
0000000000000000000010111111100000000001011111111110101110000000000000000									
Data Register									
0	00	00	00	00	00	00	00	00	00
1	00	00	00	00	00	00	00	00	00
2	00	00	00	00	00	00	00	00	00
3	00	00	00	00	00	00	00	00	00
4	00	00	00	00	00	00	00	00	00
5	00	00	00	00	00	00	00	00	00
6	00	00	00	00	00	00	00	00	00
7	00	00	00	00	00	00	00	00	00
Control Register									
PCCT, PC, Scevt, Sctd, Jndur, Jndev, Rep, Next, PCJump, PCG, MICK									
0	0	10	,00	01	0	0	1	00D	000
1	0	off,	00	00	0	0	1	1000	000
2	0	off,	00	00	0	10	1	000	000
3	0	15	,00	00	0	15	0	000	000
4	0	00	,00	00	0	00	00	000	000
5	0	00	,00	00	0	00	00	000	000
6	0	00	,00	00	0	00	00	000	000
7	0	00	,00	00	0	00	00	000	000
8	0	00	,00	00	0	00	00	000	000
9	0	00	,00	00	0	00	00	000	000
10	0	00	,00	00	0	00	00	000	000
11	0	00	,00	00	0	00	00	000	000
12	0	00	,00	00	0	00	00	000	000
13	0	00	,00	00	0	00	00	000	000
14	0	00	,00	00	0	00	00	000	000
15	0	00	,00	00	0	00	00	000	000
Config store n page									
ICtx, Scevt, Sctd, Cndx, RegMap									
0	00	p0 p2 p4							
1	00	p0 p2 p4							
2	00	p0 p2 p4							
3	00	p0 p2 p4							
4	00	p0 p2 p4							
5	00	p0 p2 p4							
6	00	p0 p2 p4							
7	00	p0 p2 p4							
8	00	p0 p2 p4							
9	00	p0 p2 p4							
10	00	p0 p2 p4							
11	00	p0 p2 p4							
12	00	p0 p2 p4							
13	00	p0 p2 p4							
14	00	p0 p2 p4							
15	00	p0 p2 p4							

FIG 5

650

Symbols for cmodel0 on npe0

```
|--W:/cmodels/npe0_sw_tb/win32/txpcr.s
data_label Bar 0x0004
|---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket 76 4
    .pkthdr2 0X4 (0X4)=0xxxxxxxxx
    .pkthdr1 0X13 (0X10)=0xxxxxxxxx
    .mp
        |---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket1 46 4
            .pkthdr3 0X22 (0X20)=0xxxxxxxxx
            .pkt6
                |---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
                    .pkthdr4 0X39 (0X38)=0xxxxxxxxx
                    .pkt1
                        |---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
                            .pkthdr5 0X50 (0X50)=0xffffffff
                            .pkthdr6 0X68 (0X68)=0xffffffff
                            .pkthdr7 0X00 (0X0)=0x20
                            .data_label ConfigTable1(word) 0X50 (0X50)=0xffffffff
                            .data_label ConfigTable2(word) 0X68 (0X68)=0xffffffff
                            .data_label ConfigTable91(*) (word) 0X0 (0X0)=0x20
                            .data_reg xxx d0(word) (d0)=00 00 00 00
                            .data_reg region53.xxx d4(word) (d4)=00 00 00 00
                            .index_reg region53.myireg i2 (i2)=0000 0000
                            .index_reg region53.region58.myireg i4 (i4)=0000 0000
                            .code_label CodeLabel3=0x0003
                            .code_label end=0x002a
|---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p9 4 4
|---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
|---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket1 46 4
    .pkthdr3 0X0
        .pkt6
            |---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
                .pkthdr4 0X17
                .pkt1
                    |---struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
                    .pkthdr5 0X0 (0X0)=0xffffffff
                    .pkthdr6 0X0 (0X0)=0xffffffff
                    .pkthdr7 0X0 (0X0)=0xffffffff
                    .data_label Bar 0x0004
                    .data_label ConfigTable1(word) 0X50 (0X50)=0xffffffff
                    .data_label ConfigTable2(word) 0X68 (0X68)=0xffffffff
                    .data_label ConfigTable91(*) (word) 0X0 (0X0)=0x20
                    .data_reg xxx d0(word) (d0)=00 00 00 00
                    .data_reg region53.xxx d4(word) (d4)=00 00 00 00
                    .index_reg region53.myireg i2 (i2)=0000 0000
                    .index_reg region53.region58.myireg i4 (i4)=0000 0000
                    .code_label CodeLabel3=0x0003
                    .code_label end=0x002a
```

FIG. 6

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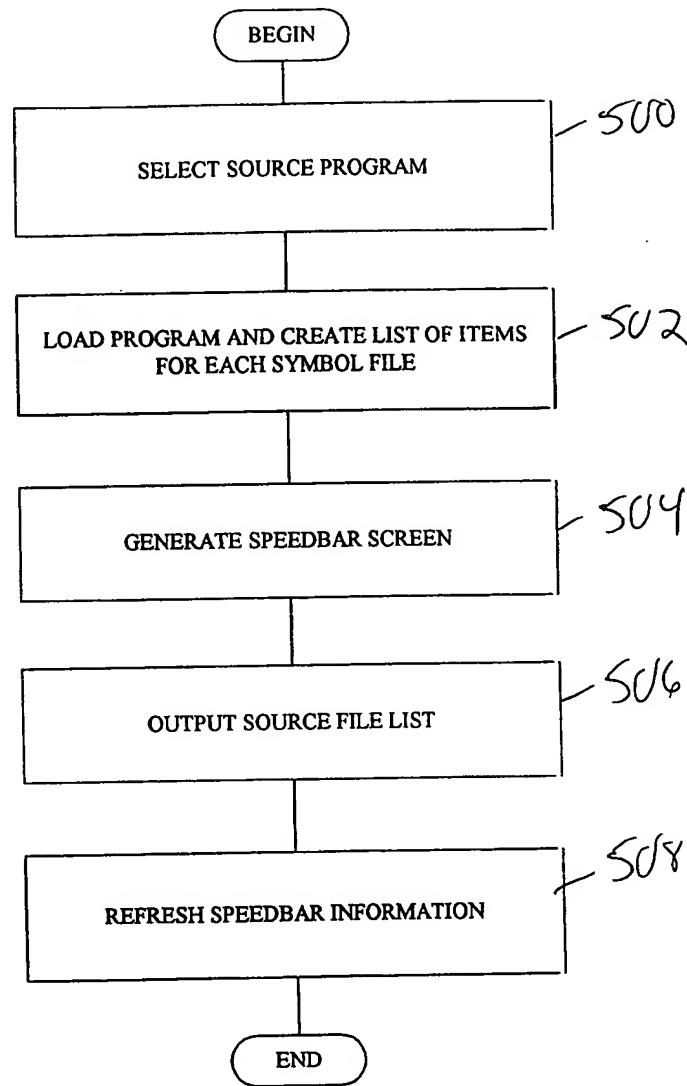


FIG. 7

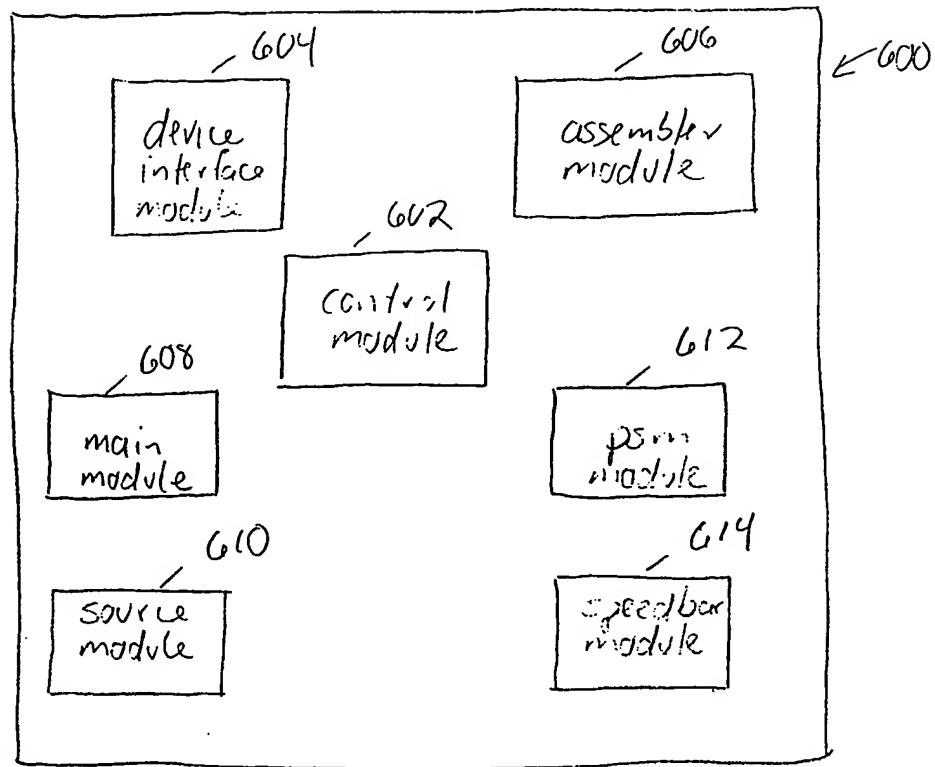


FIG. 8